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Fig. 1

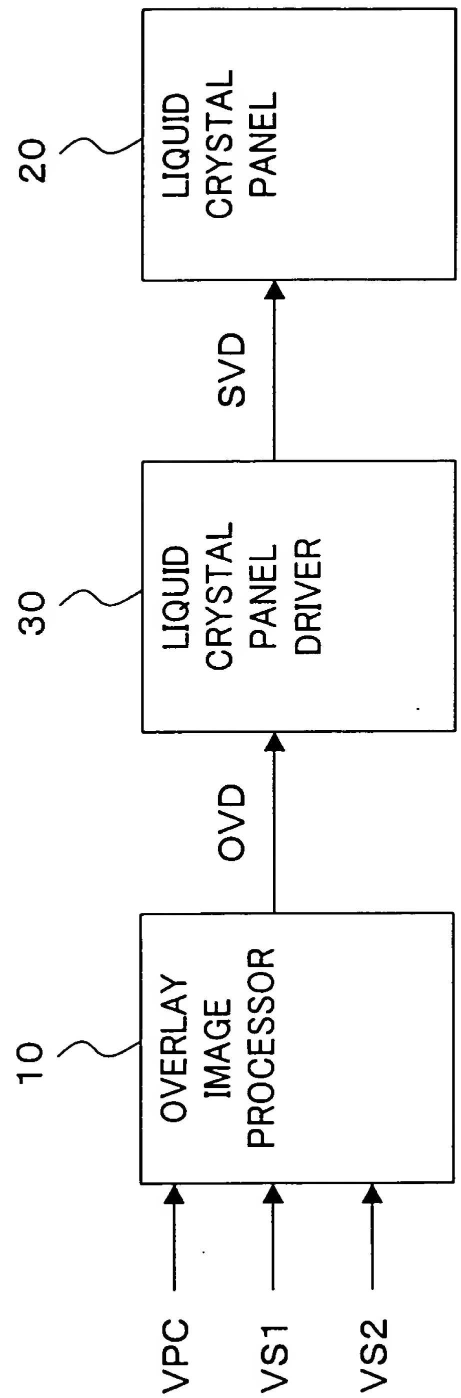

Fig. 2

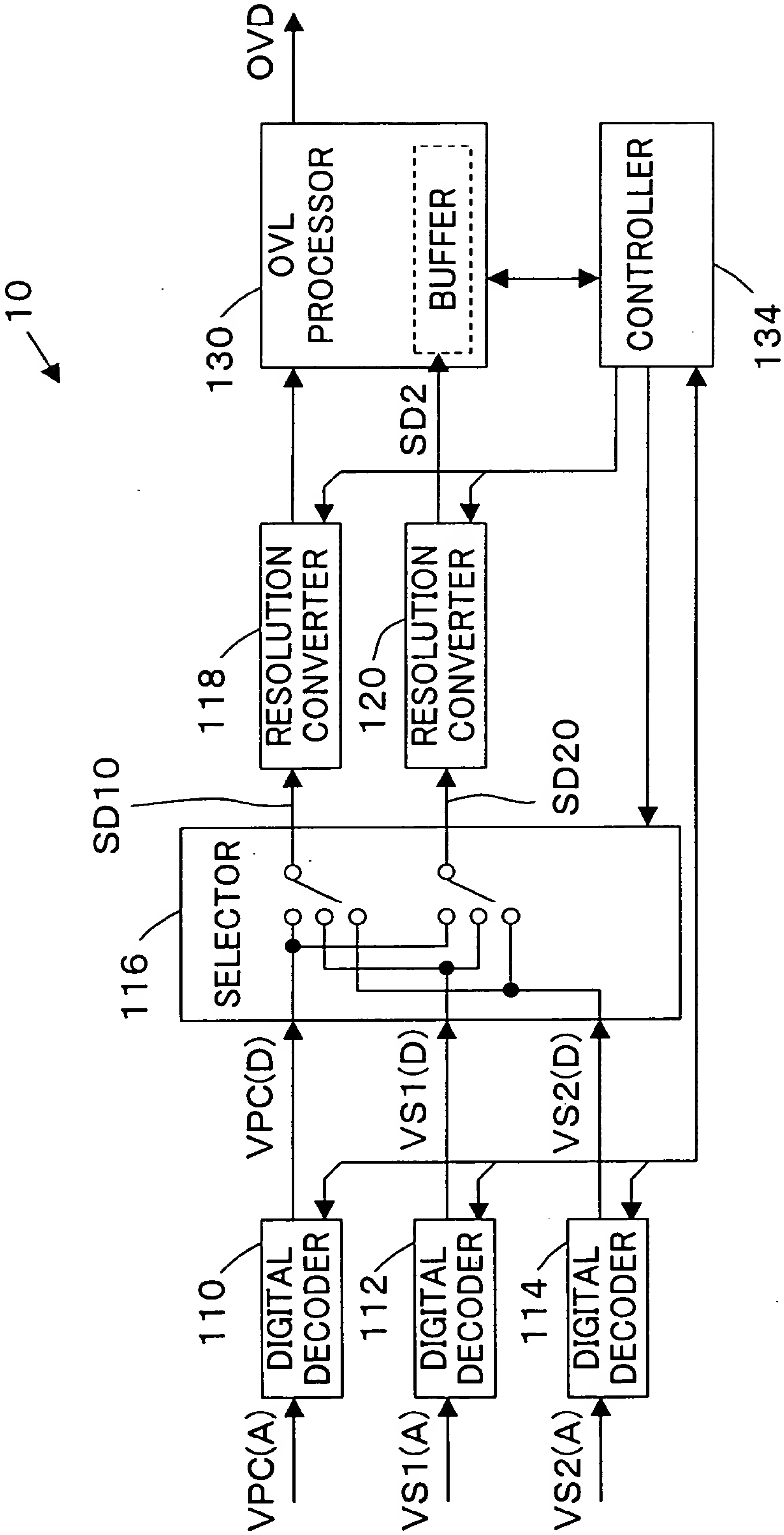
10
VPC(A)
VS1(A)
VS2(A)
110
DIGITAL DECODER
112
DIGITAL DECODER
114
DIGITAL DECODER
VPC(D)
VS1(D)
VS2(D)
116
SELECTOR
SD10
SD20
118
RESOLUTION CONVERTER
120
RESOLUTION CONVERTER
SD2
130
OVL PROCESSOR
BUFFER
OVD
CONTROLLER
134

Fig. 3

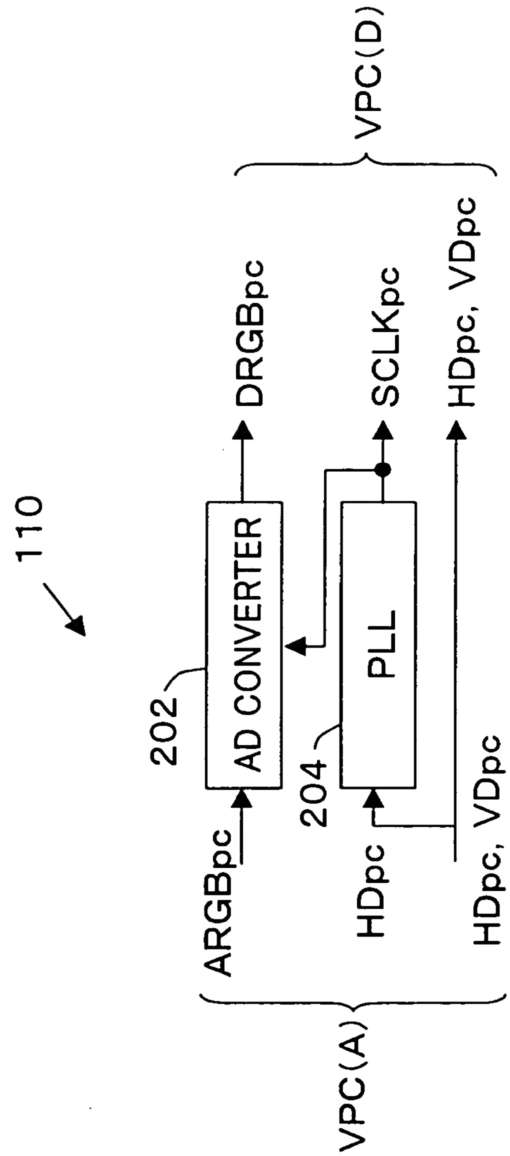
110
202
AD CONVERTER
204
PLL
ARGBpc
HDpc
HDpc, VDpc
DRGBpc
SCLKpc
HDpc, VDpc
VPC(A)
VPC(D)

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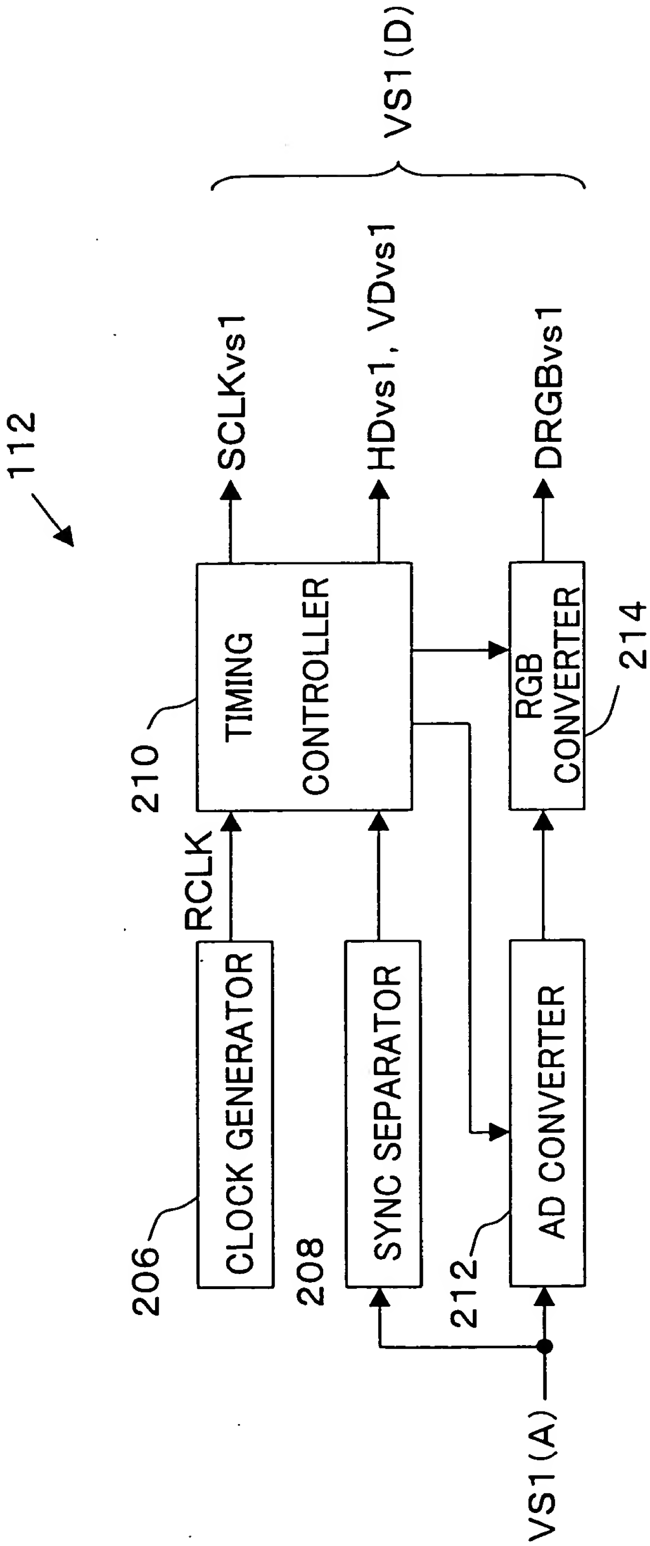
Fig. 4



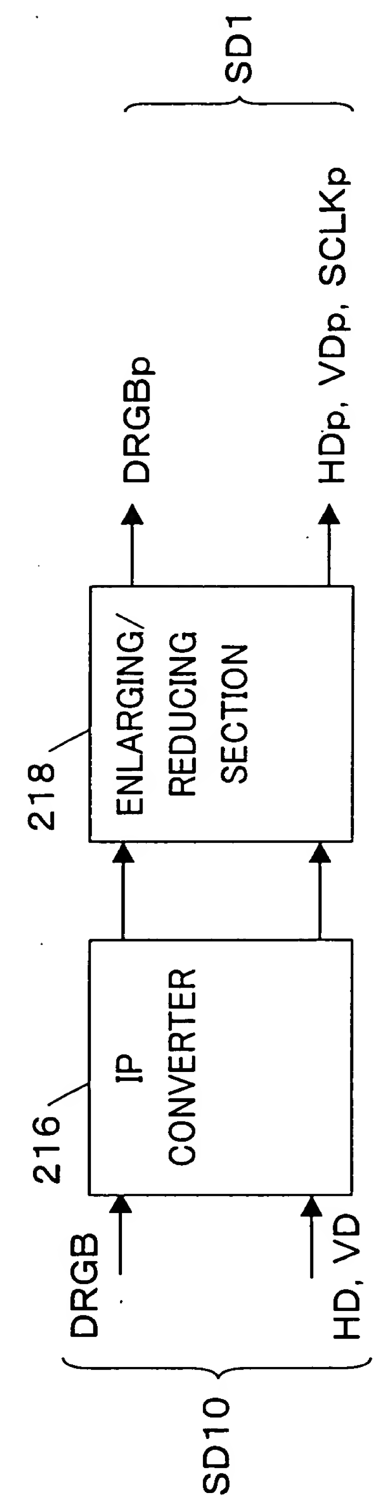
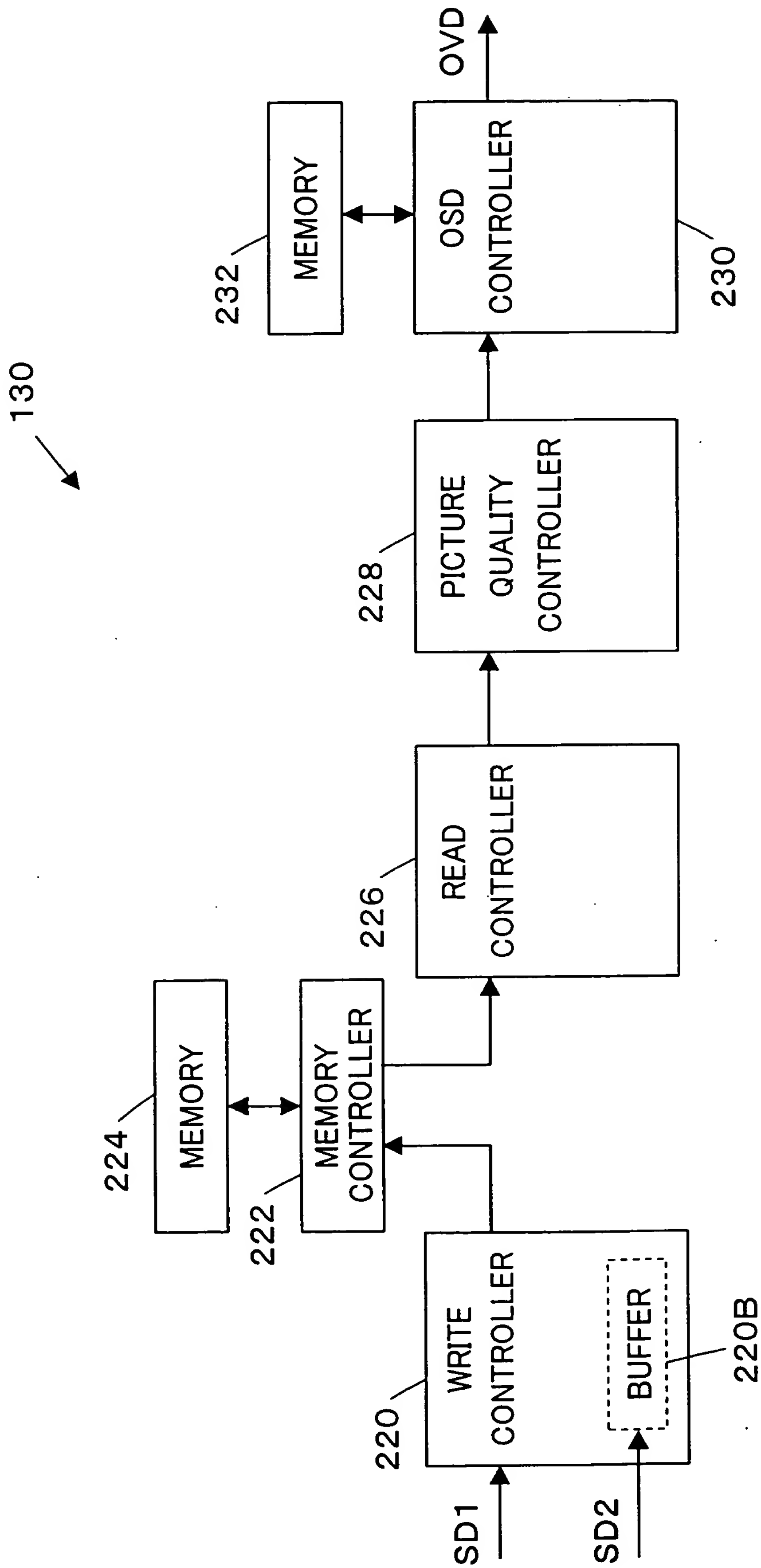

118
SD10
DRGB
HD, VD
216
IP CONVERTER
218
ENLARGING/ REDUCING SECTION
DRGBp
HDp, VDp, SCLKp
SD1

Fig.5

Fig. 6


130
SD1
SD2
220
WRITE CONTROLLER
BUFFER
220B
222
MEMORY CONTROLLER
224
MEMORY
226
READ CONTROLLER
228
PICTURE QUALITY CONTROLLER
230
OSD CONTROLLER
232
MEMORY
OVD

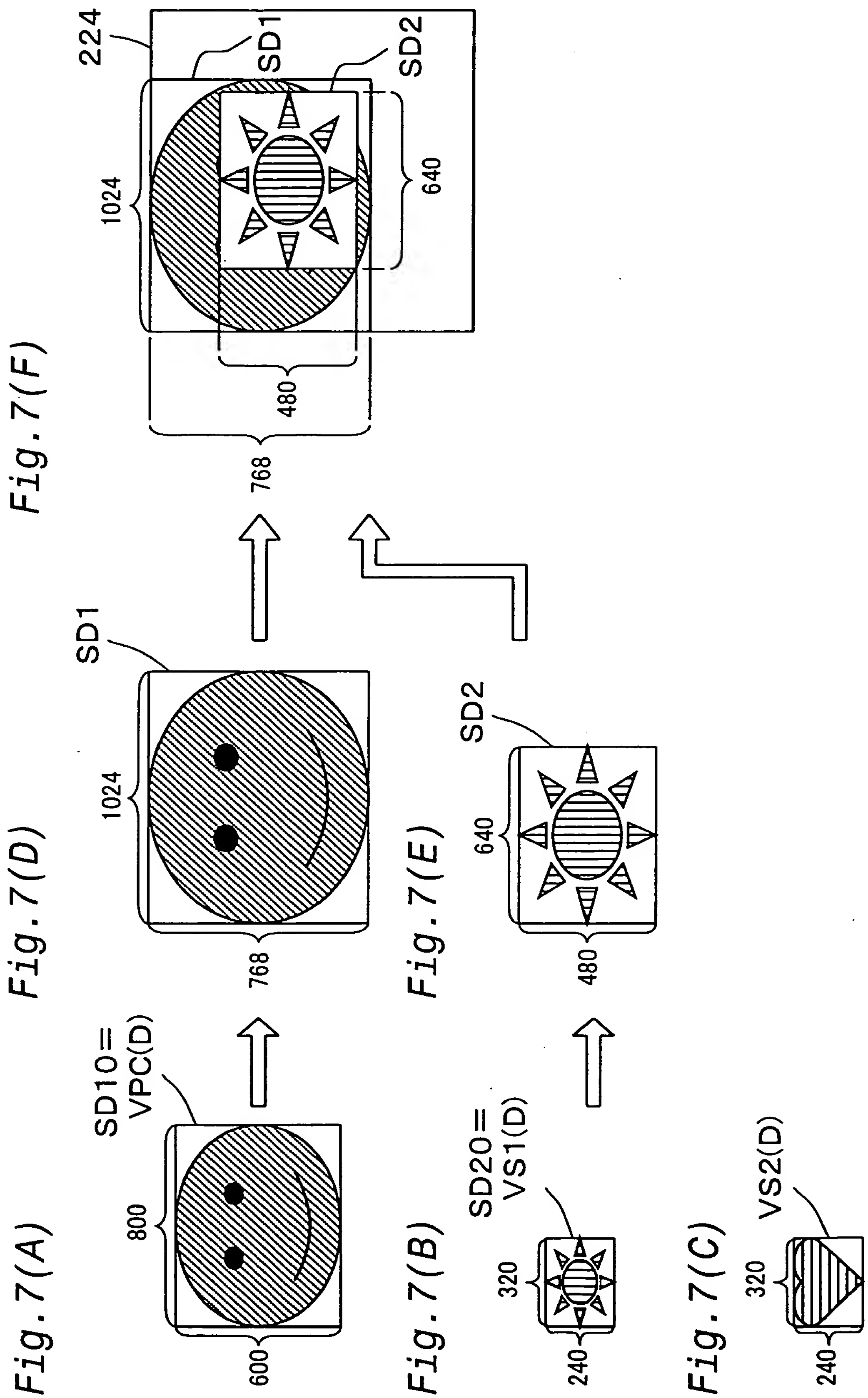


Fig.7(A)

Fig.7(B)

Fig.7(C)

Fig.7(D)

Fig.7(E)

Fig.7(F)

Fig. 8(A)

SD1:VPC, SD2:VS1

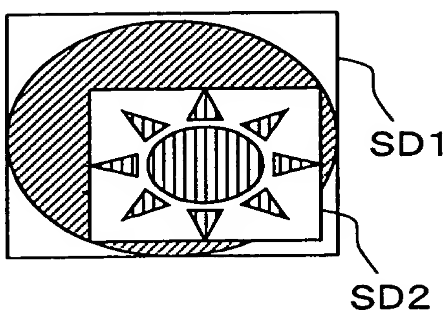


Fig. 8(B)

SD1:VPC, SD2:VS2

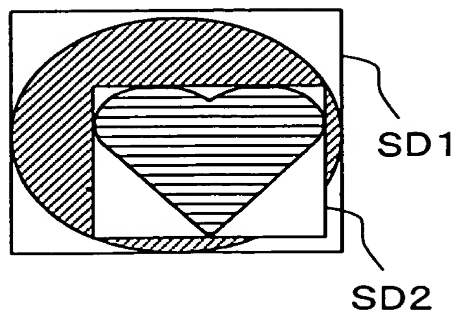


Fig. 8(C)

SD1:VS1, SD2:VS2

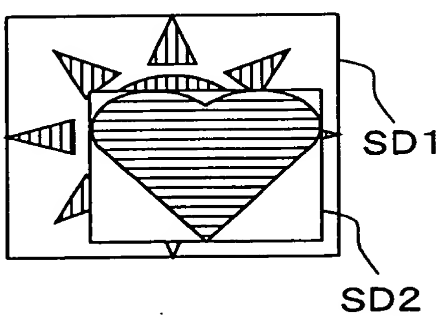


Fig. 8(D)

SD1:VS1, SD2:VPC

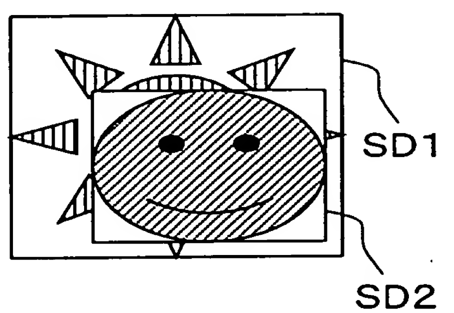


Fig. 8(E)

SD1:VS2, SD2:VS1

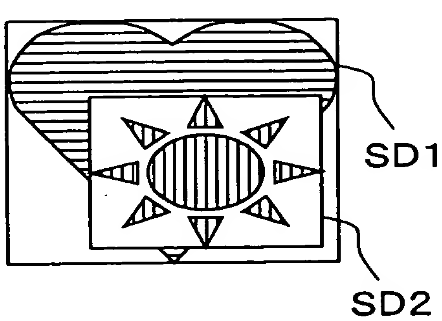


Fig. 8(F)

SD1:VS2, SD2:VPC

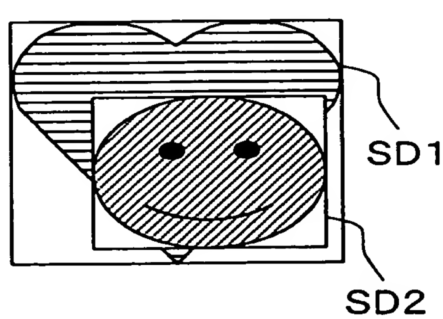


Fig.9

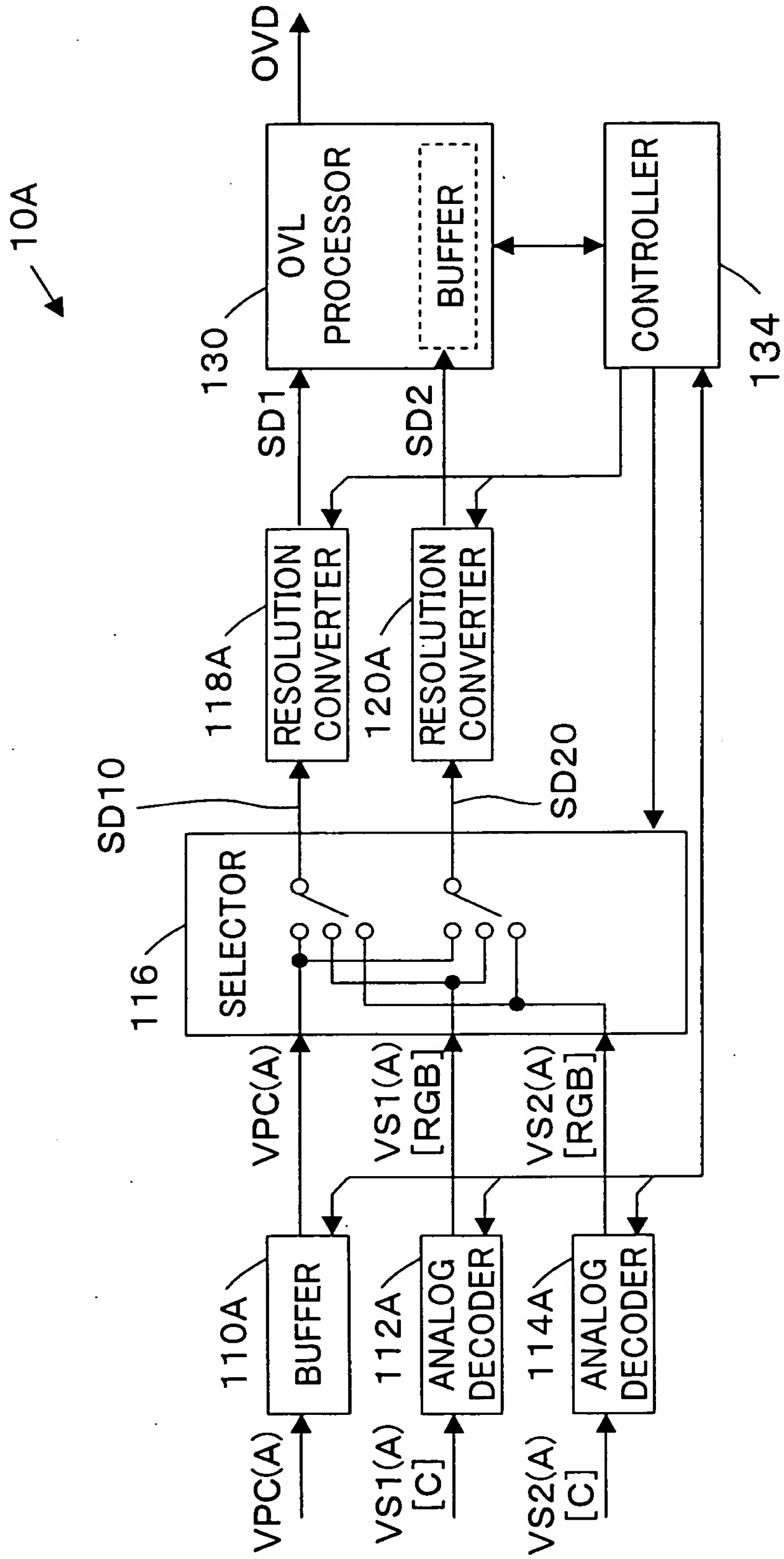

10A
OVD
OVL PROCESSOR
BUFFER
130
CONTROLLER
134
SD1
SD2
RESOLUTION CONVERTER
118A
RESOLUTION CONVERTER
120A
SD10
SD20
SELECTOR
116
VPC(A)
VS1(A) [RGB]
VS2(A) [RGB]
BUFFER
110A
ANALOG DECODER
112A
ANALOG DECODER
114A
VPC(A)
VS1(A) [C]
VS2(A) [C]

Fig.10

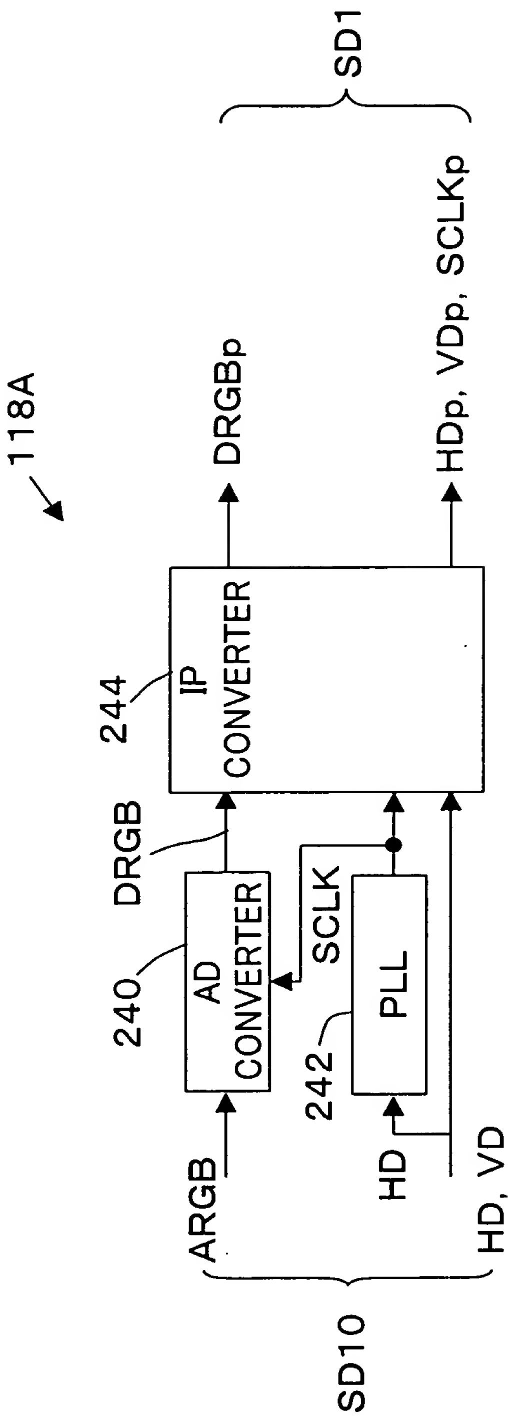

118A
SD1
DRGBp
HDp, VDp, SCLKp
244
IP CONVERTER
DRGB
240
AD CONVERTER
SCLK
242
PLL
ARGB
HD
HD, VD
SD10

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Fig.11

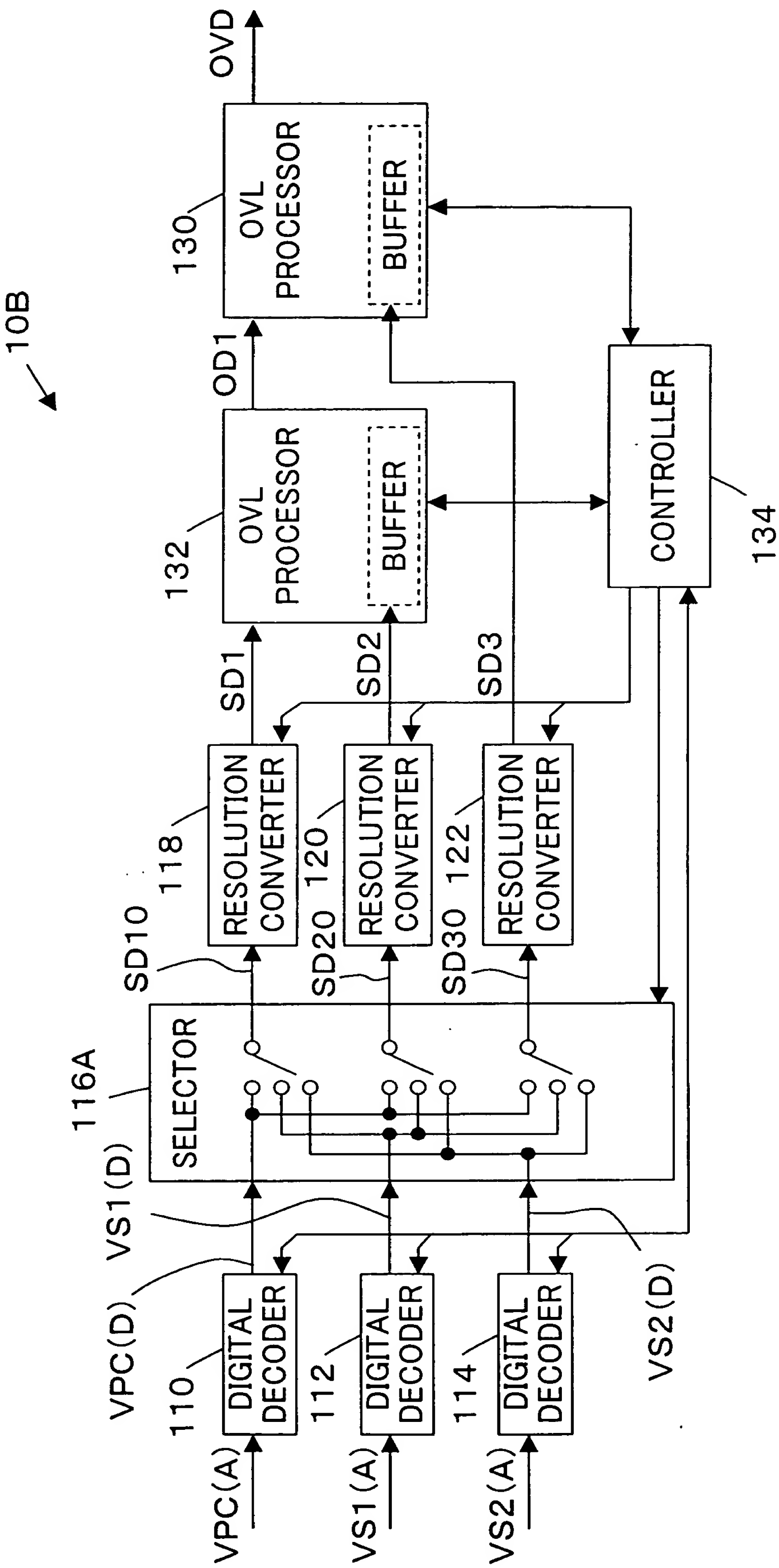


Fig.12(A)

SD1:VPC, SD2:VS1
SD3:VS2

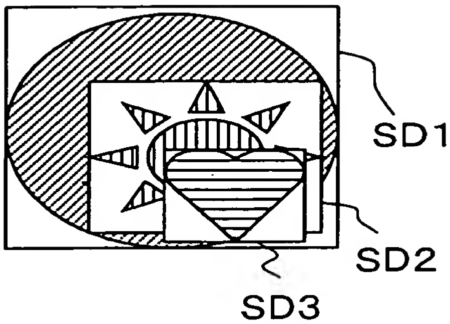


Fig.12(B)

SD1:VPC, SD2:VS2
SD3:VS1

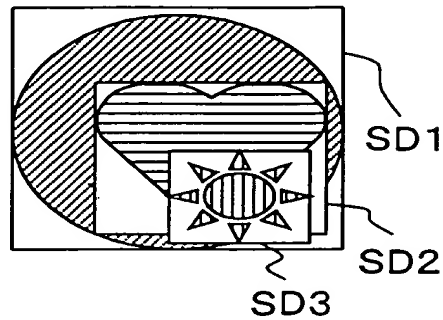


Fig.12(C)

SD1:VS1, SD2:VS2
SD3:VPC

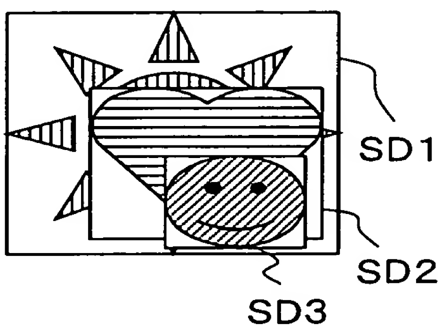


Fig.12(D)

SD1:VS1, SD2:VPC
SD3:VS2

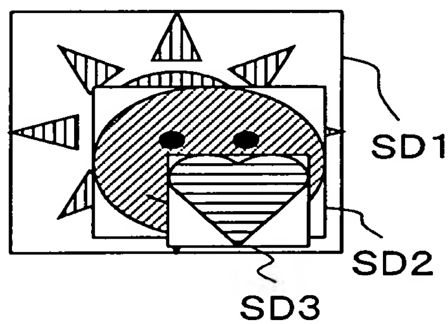


Fig.12(E)

SD1:VS2, SD2:VS1
SD3:VPC

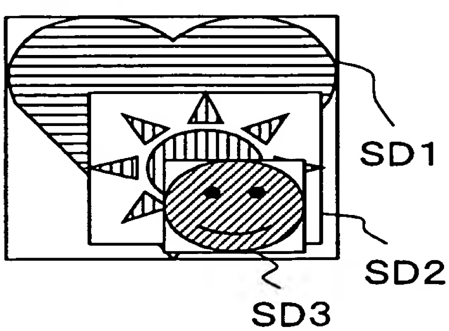


Fig.12(F)

SD1:VS2, SD2:VPC
SD3:VS1

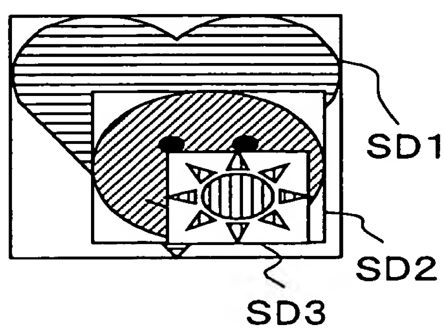
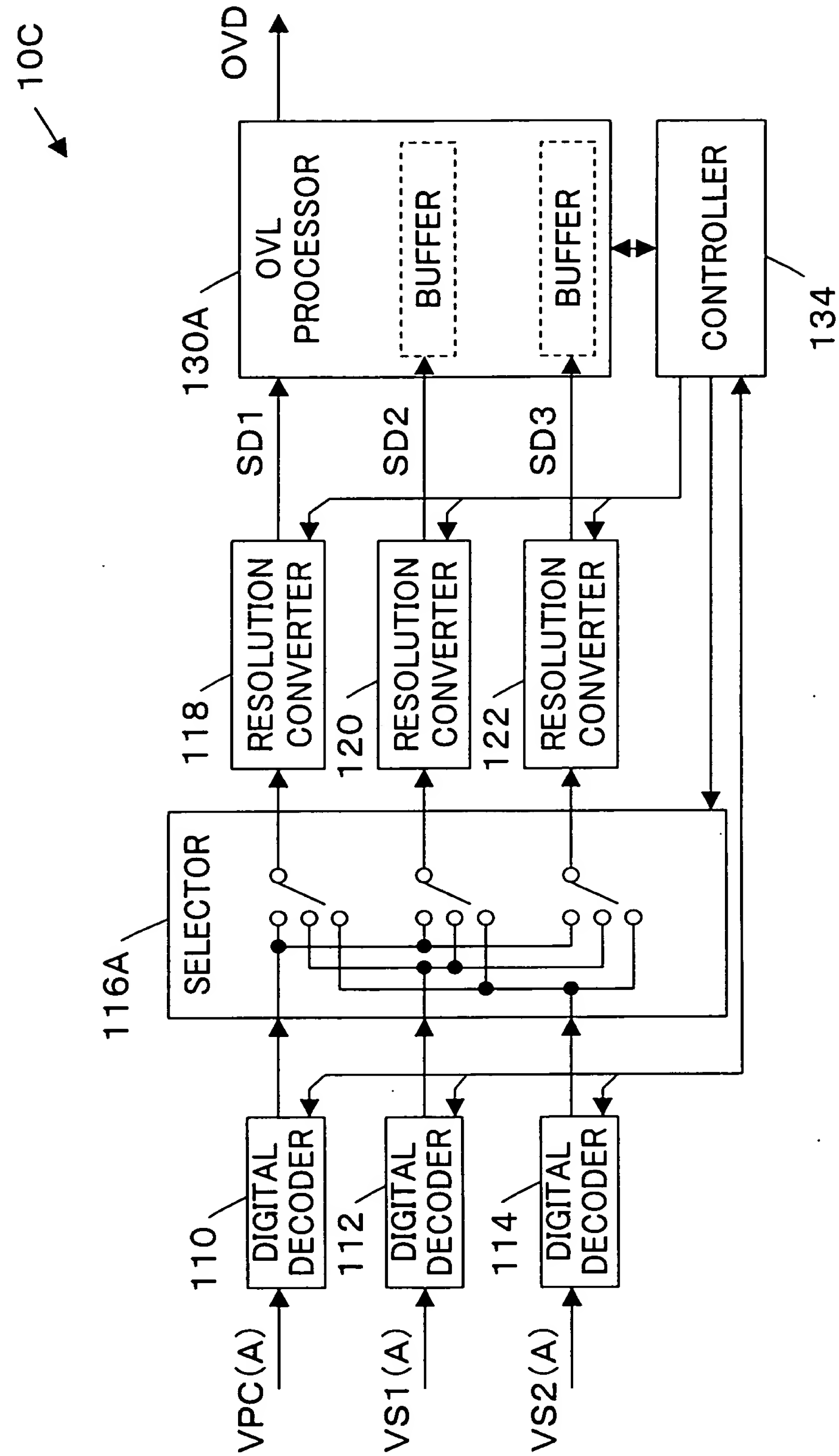


Fig. 13


10C
VPC(A)
VS1(A)
VS2(A)
110
112
114
DIGITAL DECODER
DIGITAL DECODER
DIGITAL DECODER
116A
SELECTOR
118
120
122
RESOLUTION CONVERTER
RESOLUTION CONVERTER
RESOLUTION CONVERTER
SD1
SD2
SD3
130A
OVL PROCESSOR
BUFFER
BUFFER
OVD
CONTROLLER
134